

## **IN THE CLAIMS:**

The present status of the pending claims is listed below.

1. (Currently Amended) A system comprising:
  - an integrated circuit;
  - a printed circuit board (PCB) including at least one signal layer for conveying signals to and from the integrated circuit;
  - a power laminate ~~including at least one power plane and at least one reference plane~~ for providing core power to the integrated circuit, wherein the power laminate includes a plurality of plane pairs, wherein each of the plurality of plane pairs includes a power plane and a reference plane, and wherein the power laminate is separate from the PCB;
  - a voltage regulator circuit mounted upon the power laminate, the voltage regulator circuit configured for receiving a first voltage and providing a second voltage to the power laminate, wherein the second voltage is the core power voltage; and
  - a plurality of decoupling capacitors mounted upon the power laminate.
2. (Original) The system as recited in claim 1, wherein the PCB is not configured for providing core power to the integrated circuit.
4. (Original) The system as recited in claim 1, wherein the power laminate is arranged between the integrated circuit and the PCB.
5. (Original) The system as recited in claim 4, wherein the power laminate includes an aperture for allowing signals to pass from the PCB to the integrated circuit.
6. (Original) The system as recited in claim 1, wherein the power laminate is mounted to the PCB by soldering.

7. (Original) The system as recited in claim 6, wherein the power laminate includes a ball-grid array for mounting to the PCB.
8. (Original) The system as recited in claim 1, wherein the power laminate includes a land-grid array for mounting to the PCB.
9. (Original) The system as recited in claim 1, wherein the power laminate includes a dielectric layer arranged between the power plane and the reference plane.
10. (Cancelled)
11. (Currently Amended) The system as recited in claim ~~10~~ 11, wherein each of the ~~power~~ plurality of plane pairs is in an electrically parallel configuration with respect to each of the other plane pairs of the plurality of ~~power~~ plane pairs.
12. (Withdrawn)
13. (Original) The system as recited in claim 1, wherein the voltage regulator circuit is a switching voltage regulator.
14. (Withdrawn)
15. (Original) The system as recited in claim 1 further comprising a voltage regulator module, the voltage regulator module including the voltage regulator circuit.
16. (Canceled)
17. (Original) The system as recited in claim 1, wherein the decoupling capacitors are surface mounted capacitors.